COURT-II IN THE APPELLATE TRIBUNAL FOR ELECTRICITY (Appellate Jurisdiction)

APPEAL NO. 119 OF 2018 & IA NOS. 577 OF 2018 & 169 OF 2019

Dated : 4th February, 2019

Present: Hon' ble Mr. Justice N.K. Patil, Judicial Member Hon' ble Mr. Ravindra Kumar Verma, Technical Member

In the matter of:

Indo Rama Synthetics Ltd			Appellant (s)
Versus			
Maharashtra Electricity Regulatory Commission & Anr.			Respondent (s)
Counsel for the Appellant(s)	:	Mr. Sanyam Saxena	

Counsel for the Respondent (s) : Ms. Divya Anand for R-2

ORDER (IA No. 169 of 2019 for delay in filing rejoinder)

The learned counsel, Mr. Sanyam Saxena, appearing for the Appellant submitted that, there is a delay of 21 days in filing rejoinders. In the light of the submissions and the reasoning given in the applications, the delay has been explained satisfactorily and sufficient cause has been shown in the application. The same may kindly be accepted and delay in filing rejoinder may kindly be condoned and the instant IA may kindly be allowed in the interest of justice and equity.

Submission of the learned counsel for the Appellant, as stated supra, is placed on record.

In the light of the submissions of the learned counsel for the Appellant and after perusal of the reasoning given in the application explaining the delay in filing rejoinder, we find it satisfactory as sufficient cause has been shown in the application. The same is accepted and the delay in filing rejoinder is condoned. IA is allowed.

<u>APPEAL NO. 119 OF 2018 &</u> <u>IA NO. 577 OF 2018</u>

The learned counsel for the Appellant and the second Respondent submitted that, pleadings in the instant case are completed and the matter may kindly be posted for final hearing.

Submissions of the learned counsel for the Appellant and the second Respondent, as stated supra, are placed on record.

List this matter for hearing on **04.04.2019**, as agreed by the learned counsel for the Appellant and the second Respondent.

(Ravindra Kumar Verma) **Technical Member**

(Justice N.K. Patil) **Judicial Member**

vt/ss